PXI 9725

▶ **Embedded PXI Bus System Controller**

Features

- **Intel® Celeron® M Processor 370**
- **IEEE488.2 GPIB Controller**
- **USB2, COM, LPT, 10/100BaseT Ethernet**
- **Windows 2000/XP, Linux**
- **LongLife™ through ETX technology**

Overview

The PXI 9725 is a full-featured PXI bus Embedded System Controller ideally suited for cost sensitive applications.

The ines PXI 9700 series is build on industry-strength ETX CPU modules. By using industry-strength CPU modules a product lifetime of typically ten (10) years can achieved. For your PXI bus application this means that there is no need to re-evaluate your application each time a new CPU generation appears.

IEEE488.2 GPIB interface

The on-board GPIB interface allows to integrate GPIB equipment with your PXI test system application without additional costs.

It performs all the basic IEEE-488.1 functions such as talker, listener and system controller. The IEEE-488.2 compatible funcions make it fully compliant with the IEEE-488.2 specification. In controller applications, you can control typically up to 15 devices (instruments). If operated as a talker/listener (device) interface it does exchange data and state information with the current controller-in-charge of the GPIB bus. The PXI 9725 lets Windows and Linux programs control GPIB devices.

Specifications

PC Architecture Components

CPU: Intel® Celeron® M Processor 370, 1500 MHz, 32 KB Level 1 Cache, 1024 KB Level 2 Cache, "Dothan" Core
System Memory: 1 x 200 pin SO-DIMM sockets, support ECC Double DataRate (DDR)128 MB to 1 GB, accept 128/256/512/1000 MB DDR200/266/333 DRAM.
System Chipset : Intel 855GME GMCH/ ICH4 Chipset 400 MHz PSB
BIOS: AWARD 4 Mbit Flash BIOS
WatchDog Timer: 255 levels timer interval, from 1 to 255 sec or min setup by software, jumperless selection, generates system reset
Enhanced IDE interface: Two channels supports up to four EIDE devices. BIOS auto-detect, PIO Mode 3 or Mode 4, UDMA66 transfer
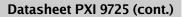
Ethernet

Chipset: Intel 82551QM
Ethernet Interface: IEEE 802.3u 100Base-T compatible, Built-in boot ROM in Flash BIOS, RJ-45 connector

Display

Chipset: Intel 855GME
Memory Size: Optimized shared memory architecture, supports up to 32 MB frame buffer using system memory
Resolution: Pixel resolution up to 1600 x 1200 at 85 Hz and 2048 x 1536 at 75 Hz

FDD interface: Flat-ribbon cable FDD connector on board
Serial Ports: One serial RS232 port (COM1)
Parallel Port: Parallel port supports SPP/EPP/ECP mode
Keyboard/mouse connector: Supports standard PC/AT keyboard and a PS/2 mouse
Power management: Supports power saving modes including Normal/Standby/Suspend modes, APM 1.2 compliant
USB: Two USB 2.0 ports
HDD: Minimum 40GB

PXI bus

Compliance: PXI Bus Rev. 2.0, CompactPCI Bus Rev. 3.0
Trigger: PXI trigger lines with flexible trigger protocols

GPIB Interface

IEEE 488.1 Capabilities: AH1, SH1, T/TE5, L/LE3, SR1, RL1, PP1/PP2, DC1, DT1, C1, C2, C3, C4, C5
IEEE 488.2 Capabilities: includes the capability to read the following bus lines:EOI, ATN, SRQ, REN, IFC, NRFD, NDAC, DAV
GPIB Handshake Rate: > 1Mbytes/sec
GPIB Connector: Standard IEEE 488 connector, no adapter cable required

Environmental and Physical

Form factor: Standard 3U PXI/CompactPCI, 12 HP wide (3-slot)
Operating ambient temperature: 0 to 50°C
Storage temperature: -20 to 80°C
Relative humidity: 5 to 95%, noncondensing
Vibration: Operation: 0.5 GRMS, 5…500 Hz, Non-operation: 1.88 GRMS, 5…500 Hz
Weight (net)**:** 0.6 kg

Ordering Information _____

PXI 9725 - Controller, Software CDROM, Windows XP
 Option -**B** - without operating system installed

On the Web_____

Click www.inesinc.com for more information and resources.

ines Test and Measurement GmbH & Co. KG
31542 Bad Nenndorf · DE (Germany)
Phone +49 5723 916 250
Fax +49 5723 916 252
Web www.inesinc.com